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U.S. UTILITY Patent Application

PATENT NUMBER and ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBC	CLASS		Ę	EXAMINER	:
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ITLE : High v	oltage semiconduc		havin	g high b	reakdow	n voltage ar	nd method	of fabricating
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		Assistant Examiner	Total Claims Print Cl. O.G			Claim for		
ISSUE FEE			DRAWING					
Amount Due	Date Paid	· .	Sheets Drwg.	Figs.Orv	VQ.	Print Fig.		
TERMINAL		Primary Examiner						
		PREPARED FOR ISSUE Application Examiner						
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